

<b>Form PTO-1449</b>			ATTORNEY DOCKET NO. <b>AUS920030412US1</b>	SERIAL NO. <b>Not Assigned</b>	
<b>LIST OF PRIOR ART CITED BY APPLICANT</b> <i>(Use several sheets if necessary)</i>			APPLICANT <b>Brown et al.</b>		
			FILING DATE	GROUP ART UNIT <b>Not Assigned</b>	
<b>U.S. PATENT DOCUMENTS</b>					
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/ SUBCLASS	FILING DATE
<b>FOREIGN PATENT DOCUMENTS</b>					
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES NO
<b>OTHER PRIOR ART (Including author, title, date, pertinent page, etc.)</b>					
TH	AA	Advanced Micro Devices White Paper, "Magic Packet Technology", Publication 20213, Revision A, Issued November 1995, pp. 1-6.			
TH	AB	<a href="http://www.maxim-ic.com/TINidocs/networkedmicros.cfm">http://www.maxim-ic.com/TINidocs/networkedmicros.cfm</a> , "Networked Microcontrollers, Solutions for Integrating Ethernet into Embedded Designs", 4/17/03, pp. 1-3.			
TH	AC	<a href="http://www.microsoft.com/hwdev/tech/network/netpm.asp">http://www.microsoft.com/hwdev/tech/network/netpm.asp</a> , "Power Management for Network Devices", 4/17/03, pp. 1-8.			
TH	AD	<a href="http://msdn.microsoft.com/library/en-us/dndevice/html/netpm.asp?frame=true">http://msdn.microsoft.com/library/en-us/dndevice/html/netpm.asp?frame=true</a> , "Power Management for Network Devices", 4/17/03, pp. 1-5.			
TH	AE	Intel 82550 Fast Ethernet Multifunction PCI/CardBus Controller, Product Brief, pp. 1-2.			
<b>RELATED PATENT APPLICATIONS</b>					
EXAMINER INITIAL	APPLICATION NO./ ATTY. DOCKET NO.	APPLICANT	TITLE	FILING DATE	
<b>DATE CONSIDERED</b> <b>02/06/2007</b>			<b>EXAMINER</b>	<b>/Tauqir Hussain/</b>	
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.					